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Teri McDonald 9/1/05

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Re Applic of	Louis Hsu, et al.
Docket No.	FIS920030348US1
Serial No.	10/707,200
Filing Date	11/26/03
Attorney	H. Daniel Schnurmann

Attached: Response to Restriction Requirement

PLEASE DELIVER TO: Theresa T. Doan
EXAMINER: ART UNIT: 2814
CONFIRMATION NO.: 1199
PHONE NO:
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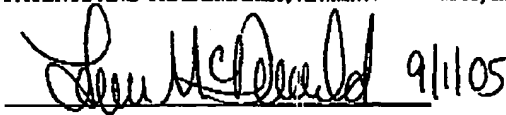
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Hopewell Junction
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Signature & Date

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE	
In re application of: Louis C. Hsu, et al.	Date: September 1, 2005
Serial Number: 10/707,200	Examiner: Theresa T. Doan
Filed: 11/26/2003	Group Art Unit: 2814
Title: Ultra-thin SOI MOSFET Method and Structure.	IBM Corporation D/18G, B/300, Zip 482 2070 Route 52 Hopewell Junction, NY 12533-6531

RESPONSE TO RESTRICTION REQUIREMENT

Commissioner of Patents and Trademarks
P. O. Box 1450
Alexandria, VA 22313-1450

Sir:

This is in response to the Office Action dated August 23, 2005.

The Examiner in the aforementioned Office Action has required restriction under
35 U.S.C. § 121, stating that the claims belong to:

GROUP I, Claims 1-11 and 18, drawn to a method of forming a MOSFET
semiconductor device, and

GROUP II, Claims 12-17, drawn to a MOSFET transistor.

10/707,200


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Applicants elect to prosecute the invention of GROUP I, consisting of Claims 1-11 and 18 drawn to the method of forming the MOSFET device, and withdraw from consideration the claims forming GROUP II, as being drawn to non-elected invention, without prejudice to the Applicants' right to file a Divisional or Continuation or Continuation-in-Part patent application for the withdrawn claims.

The election is made without traverse.

Respectfully submitted,
LOUIS C. HSU, ET AL.

By: 
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